

Programme
14th HiPEAC Workshop on Reconfigurable Computing

Date: 20st January 2020

Room: Vetro

	Morning Session - Open Source FPGAs
10:00-10:05	Opening Address of WRC
10:05-10:35	PULPino microprocessor system Davide Rossi - University of Bologna, Italy
10:35-11:05	RISC-V/CGRA-based open source SoC José Teixeira de Sousa - INESC-ID, Portugal
11:05-11:30	<i>Coffee break</i>
11:30-12:00	Xilinx open-source research Cathal McCabe - Xilinx
12:00-12:30	Scalable performance by scaling out FPGA systems via inexpensive interconnects and the open-source AXIOM software stack Roberto Giorgi - University of Siena, Italy
12:30-13:00	TBD Dirk Koch - University of Manchester, UK
13:00-14:00	<i>Lunch</i>
	Afternoon Session - FPGAs in Space
14:00-14:30	Fault-tolerant designs based on FPGA and ASIC for space Tanya Vladimirova - University of Leicester, UK
14:30-15:00	FPGA acceleration for vision-based navigation in space applications: devices, algorithms, use cases Dimitrios Soudris - NTUA, Greece
15:00-15:30	Performance and Reliability of Reconfigurable High Density Rad-Hard FPGAs for Space Luca Sterpone - Politecnico di Torino, Italy
15:30-16:00	<i>Coffee break</i>

16:00-16:30	European Processor and the role of eFPGA Imen Baili, Menta S.A.S.
16:30-17:00	Increasing reliability of commercial reconfigurable MPSoC FPGAs for space applications Eduardo de la Torre - Universidad Politécnica de Madrid, Spain
17:00-17:30	Panel Discussion