

## ON THE APPLICATION OF RESIDUE NUMBER SYSTEMS TO NEURAL NETWORKS AND CRYPTOGRAPHY

DASIP 2018 - CONFERENCE ON DESIGN AND ARCHITECTURES FOR SIGNAL  
AND IMAGE PROCESSING

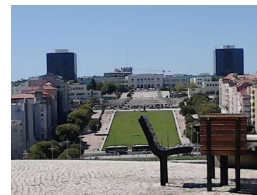
**Leonel Sousa, Paulo Martins**  
*October 2018*

## Where do I come from?

- IST
  - Faculty of Engineering / University of Lisbon
  - ~9000 / ~55000 students



- INESC-ID
  - Research institute
    - 200 PhD Researchers
    - 300 Graduate Students



## Motivation

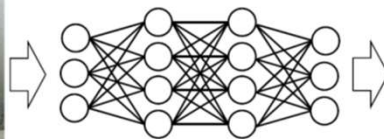
**Block Chain:** Public Key Cryptography is an essential part of Bitcoin's protocol ensure the integrity of messages created in the protocol

## Motivation

### Image Classification by NN



Input

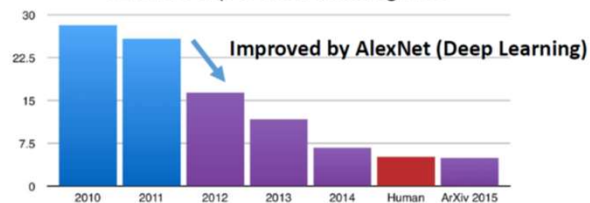


Neural Network (NN)

Cat  
(92%)

Output

ILSVRC top-5 error on ImageNet

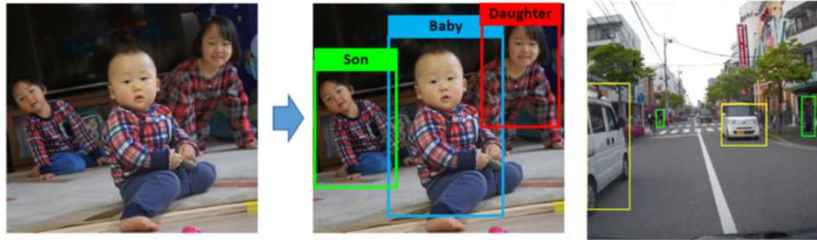


## Motivation

Deep Learning: Convolutional Neural Networks

### Object Detection

- Detect multiple objects at a time
- High performance-power is necessary



## Motivation: Computer Arithmetic



Binary System  
Carry chains limit arithmetic scaling



Residue Number System (RNS)  
Computation over several channels

## Outline

- Residue Number Systems (RNS)
- Public-key Cryptography: RSA, ECC, Lattice-based PQ approach
- Convolutional Neural Networks (CNN)
- Design Automation Tools
- On-going research projects
- Conclusions



DASIP 2018

7 12/10/2018

## RNS: Residue Number System

Based on the Chinese remainder theorem (CRT) widely used for computing with large integers: it allows replacing a computation for which one knows a bound on the size of the result by several similar computations on small integers

C. Chang, A. Molahosseini, A. Zarandi, and T. Tay, "Residue Number System - A new paradigm to datapath optimization for low-power and high-performance digital signal processing applications," IEEE Circuits and Systems Magazine, vol. 15, no. 4, pp. 26-44, November 2015



8 12/10/2018

# Residue Number System (RNS)

- RNS based on a set of relatively prime moduli: **moduli set**

$$P = \langle m_1, m_2, \dots, m_N \rangle$$

- The dynamic range  $M$  is given by:

$$M = m_1 \times m_2 \times \dots \times m_N$$

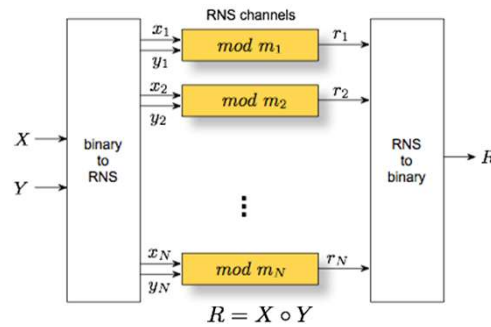
- Integer  $X$  represented as:

$$X \rightarrow \{x_1, x_2, \dots, x_N\}$$

$$x_i = X \bmod m_i$$

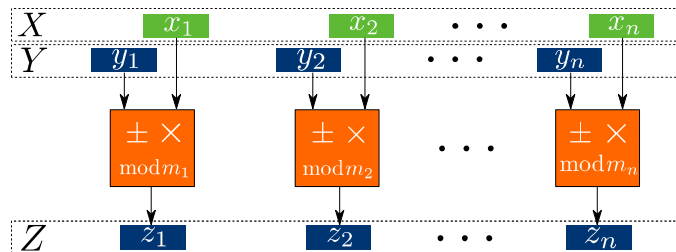
Arithmetic operations (+, -, ×, /):

$$\{r_1, r_2, \dots, r_N\} = \{(x_1 \circ y_1) \bmod m_1, (x_2 \circ y_2) \bmod m_2, \dots, (x_N \circ y_N) \bmod m_N\}$$



# RNS

- Parallelism extracted at arithmetic level
- RNS splits arithmetic modulo  $M = m_1 m_2 \dots m_n$  over  $n$  rings



## RNS

### Advantages

- Carry-free between channels
- Fast parallel  $+$ ,  $-$ ,  $\times$  and exact  $\div$
- Enables side-channel attacks counter measures

### Disadvantages

- B2R, R2B, division and modular reduction hard to perform

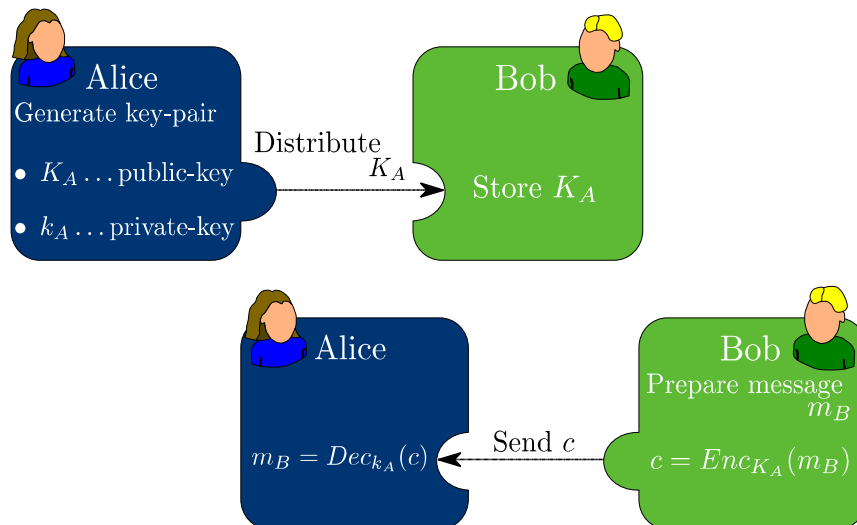
## Public-key Cryptography

**L. Sousa**, S. Antão and P. Martins, “Combining Residue Arithmetic to Design Efficient Cryptographic Circuits and Systems”, IEEE Circuits and Systems Magazine, vol. 16, n. 4, pp. 6-32, November 2016.

# Cryptographic Algorithms

- Asymmetric/Public Key Encryption algorithms
  - Cipher and decipher a block of data using a private/public key pair
  - Complex mathematical operations, computationally demanding
  - RSA, Elliptic curves, ElGamal, Post-Quantum lattice based crypto
- Digital Signatures
  - Identical to Public Key Encryption algorithms
  - Digital Signature Algorithm (DSA), RSA, Elliptic Curve Digital Signature Algorithm (ECDSA)...

# Public-key Cryptography



# RSA

## RSA Encryption and Decryption operations

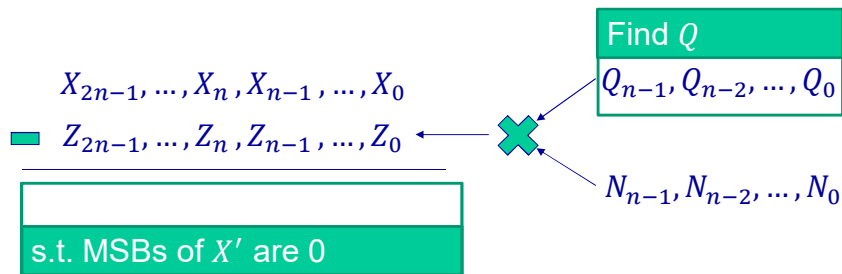
$$c = m^e \pmod{N} \longrightarrow m = c^d \pmod{N}$$

$N$  is thousands of bits wide (e.g. 4096 bits)

$$m = \left( \dots \left( (c^{d_{k-1}})^2 c^{d_{k-2}} \right)^2 \dots \right)^2 c^{d_0} \pmod{N}$$

Sequential computation

## Modular Multiplication



$$X' < 2N$$



## Montgomery Modular Multiplication

### Montgomery Domain

$${}_M X = XM \pmod{N}$$

### Arithmetic modulo $N$ is converted to modulo $M$

$$W = {}_M X {}_M Y$$

$$Q = -WN^{-1} \pmod{M}$$

$${}_M Z = \frac{W + QN}{M} \equiv XYM \pmod{N}$$
$${}_M Z < 2N$$

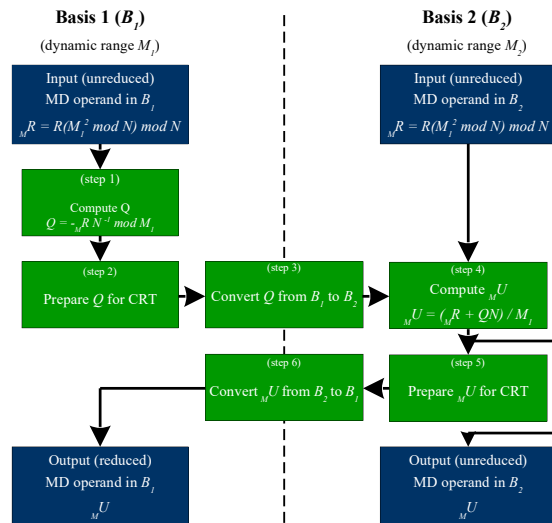
## RNS Montgomery Modular Multiplication

- Last part of computation cannot be performed modulo  $M$ :

$${}_M Z = \frac{W + QN}{M}$$

- Convert  $Q = -WN^{-1} \pmod{M}$  to the second base
- At the end,  ${}_M Z$  is converted back to the first base

## RNS Montgomery Modular Multiplication



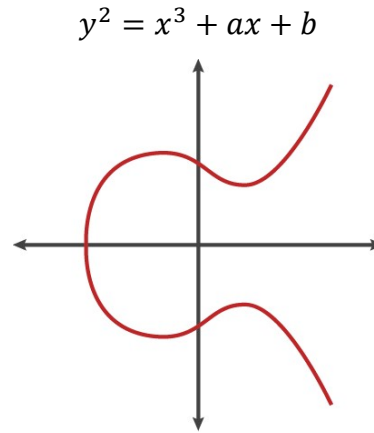
## RSA

### RSA Encryption and Decryption operations: revisited

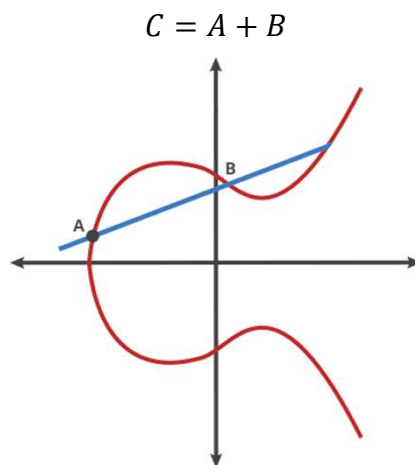
- ${}_M c = c M_1 \bmod N$
- ${}_M m = M_1 \bmod N$
- for  $i \leftarrow \{k-1, \dots, 0\}$ 
  - ${}_M m = \text{MM}({}_M m, {}_M m) \leftarrow$  Parallel computation  
if  $d_i = 1$
  - ${}_M m = \text{MM}({}_M m, {}_M c) \leftarrow$  Parallel computation
- $m = {}_M m M_1^{-1} \bmod N$

## Elliptic Curve Cryptography (ECC)

- In cryptography, these curves are defined over a finite field  $\mathbb{F}_q$



## ECC

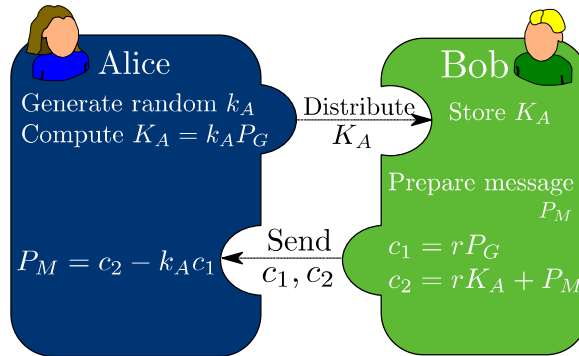


- Point multiplication is defined as

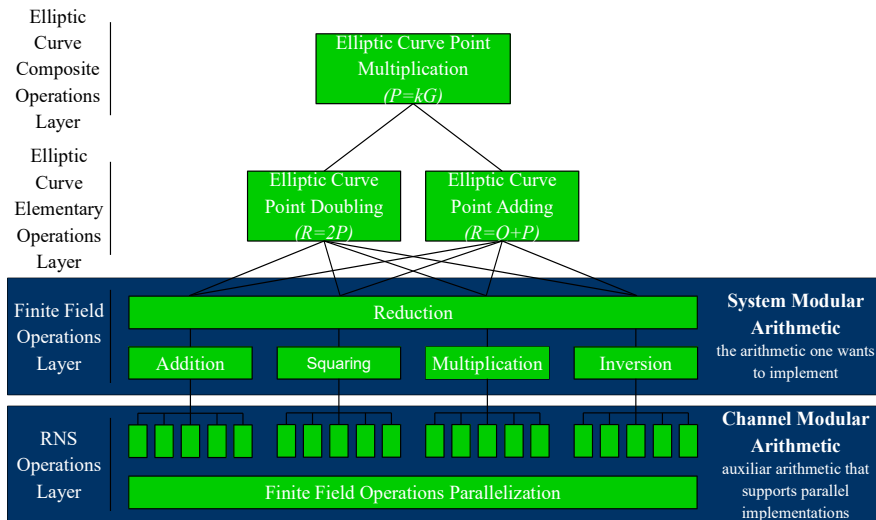
$$Q = sP = \underbrace{P + \dots + P}_{s \text{ times}}$$

- Given  $Q$  and  $P$ , it is hard to compute  $s$

# ECC Encryption/Decryption Protocol



# ECC



## ECC: Reducing Bases' Sizes

### Mersenne-like Prime

$$q = M_1^2 - 2$$

### Decompose $X$ as

$$X = K_X M_1 + R_X$$

### $M_1^2 = 2 \pmod q$ leads to

$$\begin{aligned} XY &= (K_X R_Y + K_Y R_X) M_1 + 2K_X K_Y + R_X R_Y \\ &= V M_1 + U \pmod q \\ U, V &< 3M_1^2 < M_1 M_2 \Rightarrow \text{Smaller bases required} \end{aligned}$$

## RNS based EC Point Multiplication

### Results for EC Point Multiplication in GPUs with CUDA

Reference	Platform	Lat. [ms]	T.Put [mults/s]	Observations
Szerwinski et. al.	8800 GTS	305	1413	
Bernstein et. al.	8800 GTS	-	3019	ECM factorization
Giorgi et. al.	9800 GTX	-	1972	Library eval.
RNS based	8800 GTS	30.3	3138	12 mults/block
RNS based	285 GTX	29.2	9827	20 mults/block

- An **order of magnitude** improvement in latency
- From **4% to 122% more** throughput.

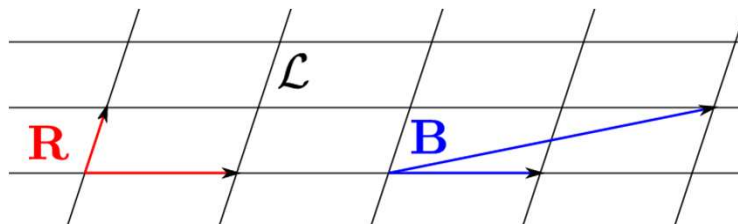
## Post Quantum Cryptography

- P. W. Shor, “Algorithms for quantum computation: discrete logarithms and factoring”, 35th Annual Symposium on Foundations of Computer Science, 1994

“...algorithms for finding **discrete logarithms** and **factoring integers** on a quantum computer that take a number of steps which is polynomial in the input size, e.g., the number of digits of the integer to be factored.”

## Lattice-based Cryptography

- A lattice is a repeated arrangement of points

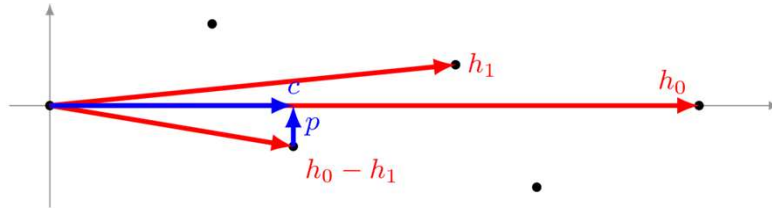


- Matrix  $R = (r_1, \dots, r_l)^T$ : a basis of  $\mathcal{L}$ 
  - $\mathcal{L} = r_1\mathbb{Z} \oplus \dots \oplus r_l\mathbb{Z}$

- For  $n \geq 2$ , there are infinite basis

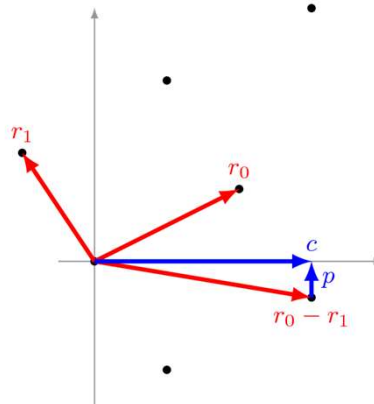
## Lattice-based Cryptography

- Encryption corresponds to adding a perturbation  $p$  to a lattice point
- $(h_0, h_1)$  is a “bad” lattice base



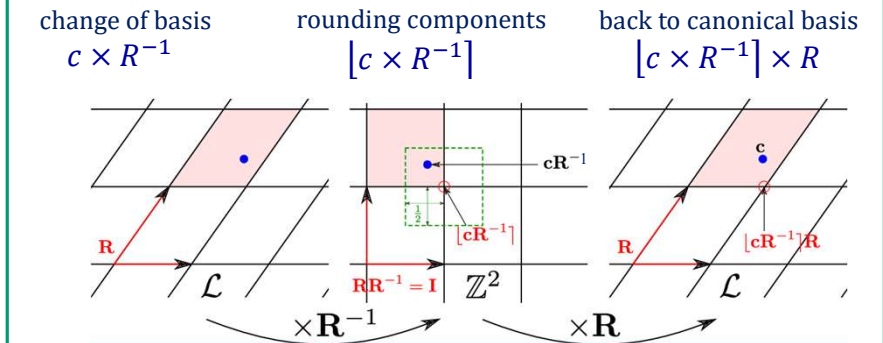
## Lattice-based Cryptography

- Decryption corresponds to finding the closest lattice vector  $u$  to  $c$  and outputting  $p = c - u$
- $(r_0, r_1)$  is a “good” lattice base



# Lattice-based Cryptography

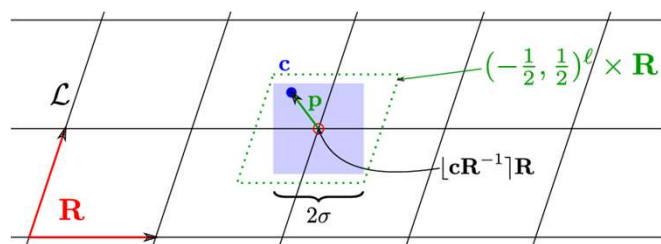
## Babai's Round-off Algorithm



# Lattice-based Cryptography

## Common Simplification Step

- Use special case of CVP: Bounded Distance Decoding Problem (BDD)
- Babai's Round-off gives the closest vector for a rotated nearly-orthogonal basis  $R$  of a lattice



$$p = c - \lfloor cR^{-1} \rfloor R \text{ mod } c \text{ } m_\sigma \text{ for } m_\sigma \geq 2\sigma + 1$$



## Lattice-based Cryptography

### GGH-like cryptosystem

- Private-key: good  $R$  s.t.  $HNF(R) = \left( \begin{array}{c|c} \det R & 0 \dots 0 \\ * & I_{n-1} \end{array} \right) = B$
- Public-key:  $B$
- Plaintext space:  $\|p\|_\infty \leq \sigma$
- Ciphertext:  $c = p \bmod B = (c_1, 0, \dots, 0)$
- Deciphering:  $p = c - \lfloor c \times R^{-1} \rfloor R =$   
 $c - \lfloor c_1 \times ((R^{-1})_{1,1}, \dots, (R^{-1})_{1,n}) \rfloor R$

## Lattice-based Cryptography

- Babai's algorithm rewritten with integer arithmetic:

$$u = \lfloor cR^{-1} \rfloor R = \left\lfloor cR^{-1} + \frac{1}{2} \right\rfloor R = \left\lfloor \frac{dcR^{-1}}{d} + \frac{1}{2} \right\rfloor R =$$

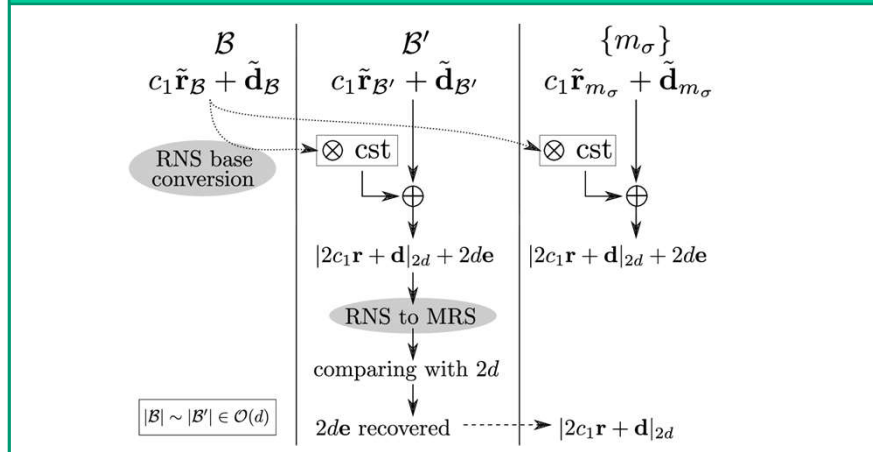
$$\frac{2cdR^{-1} + d - (2cdR^{-1} + d \bmod (2d))}{2d} R$$

where  $d = \det(R)$

Use RNS Montgomery's  
reduction

# Lattice-based Cryptography

## Principle of RNS Montgomery modular reduction



# RNS based LBC decryption

## Results for LBC decryption in CPUs/GPUs

Execution Times [ $\times 10^6$ clock cycles] (Speed-up)				
Method	$n = 400$	$n = 600$	$n = 800$	$n = 1000$
Sequential (i7 4770K)	97.51	283.8	619.4	1222
RNS-GPU (K40c)	22.97 (4.2)	283.8 (3.6)	248.9 (2.5)	512.4 (2.4)
RNS-GPU (GTX 780 Ti)	16.55 (5.9)	59.73 (4.8)	148.2 (4.2)	349.6 (3.5)
4-core RNS-CPU (i7 4770K)	21.05 (4.6)	75.48 (3.8)	189.9 (3.3)	369.7 (3.3)
4-core RNS-CPU (with AVX2) (i7 4770K)	8.668 (11.2)	29.05 (9.8)	74.79 (8.3)	148.5 (8.2)

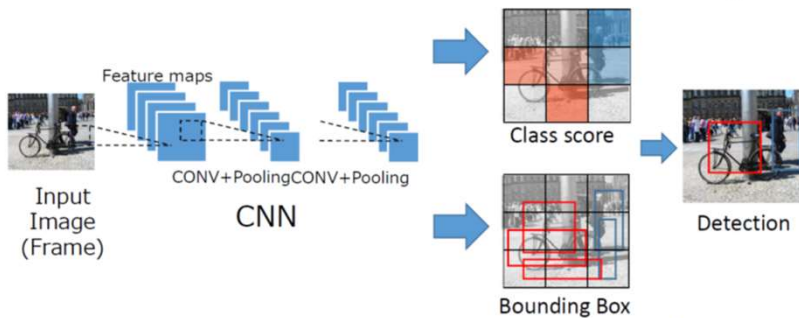
# Convolution Neural Networks

H. Nakahara and T. Sasao, "A High-speed Low-power Deep Neural Network on an FPGA based on the Nested RNS: Applied to an Object Detector," 2018 IEEE International Symposium on Circuits and Systems (ISCAS), 2018

## Deep Learning: CNNs

### YOLOv2 (You Only Look Once version 2)

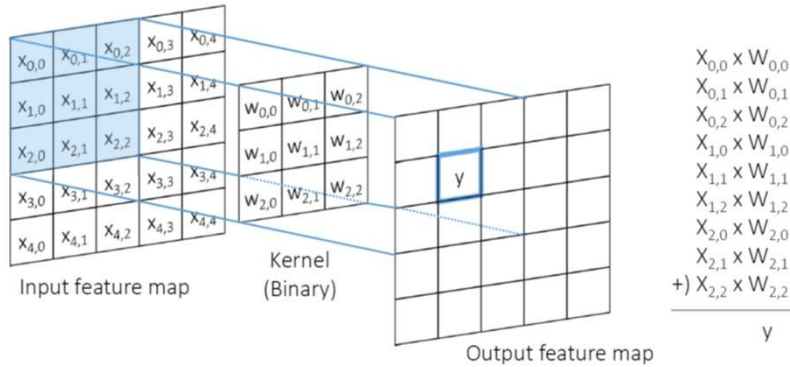
- Single CNN (One-shot) object detector
  - Both a classification and a BBox estimation for each grid



# Deep Learning: CNNs

## 2D Convolutional Operation

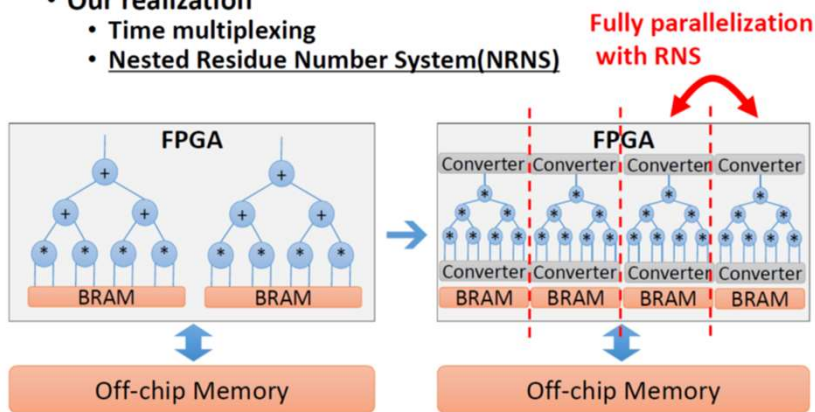
- Computational intensive part of the YOLOv2



# Deep Learning: CNNs

## Realization of 2D Convolutional Layer

- Requires more than billion MACs
- Our realization
  - Time multiplexing
  - Nested Residue Number System(NRNS)



## Nested RNS

### Nested RNS

- $(Z_1, Z_2, \dots, Z_i, \dots, Z_L) \rightarrow (Z_1, Z_2, \dots, (Z_{i1}, Z_{i2}, \dots, Z_{ij}), \dots, Z_L)$

- Ex:  $\langle 7, \underline{11}, \underline{13} \rangle \times \langle 7, 11, 13 \rangle$

$$\langle 7, \langle 5, 6, 7 \rangle_{11}, \langle 5, 6, 7 \rangle_{13} \rangle \times \langle 7, \langle 5, 6, 7 \rangle_{11}, \langle 5, 6, 7 \rangle_{13} \rangle$$

Original modulus

1. **Reuse** the same moduli set
2. **Decompose** a large modulo into smaller ones

## Nested RNS

### Example of Nested RNS

- $19 \times 22 (=418)$  on  $\langle 7, \langle 5, 6, 7 \rangle_{11}, \langle 5, 6, 7 \rangle_{13} \rangle$

$$19 \times 22$$

$$= \langle 5, 8, 6 \rangle \times \langle 1, 0, 9 \rangle$$

Binary2NRNS Conversion

$$= \langle 5, \langle 3, 2, 1 \rangle_{11}, \langle 1, 0, 6 \rangle_{13} \rangle \times \langle 1, \langle 0, 0, 0 \rangle_{11}, \langle 4, 3, 2 \rangle_{13} \rangle$$

$$= \langle 5, \langle 0, 0, 0 \rangle_{11}, \langle 4, 0, 5 \rangle_{13} \rangle$$

Modulo Multiplication

$$= \langle 5, 0, 2 \rangle$$

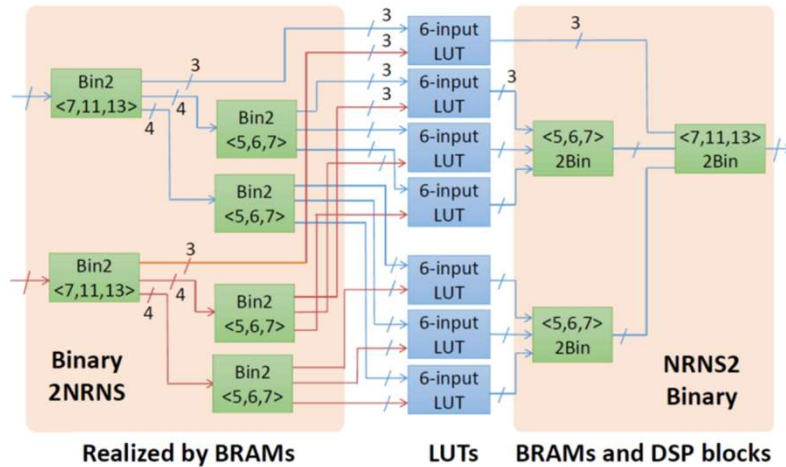
Bin2RNS on NRNS

$$= 418$$

RNS2Bin

## Nested RNS

### Realization of Nested RNS



## NRNS based YOLOv2

### NRNS based YOLOv2

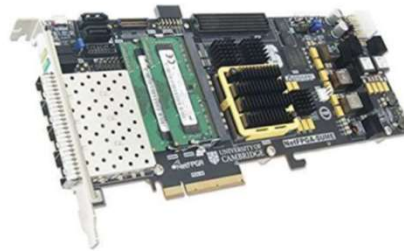
- Framework: Chainer 1.24.0
- CNN: Tiny YOLOv2
- Benchmark: KITTI vision benchmark
- mAP: 69.1 %

Layer	# In. Fmaps	# Out. F Size
(Feature Extraction)		
Conv1	3	128 × 128
Conv2	128	128 × 128
Max Pool	128	64 × 64
Conv3	128	64 × 64
Conv4	128	64 × 64
Conv5	128	64 × 64
Max Pool	128	32 × 32
Conv6	128	32 × 32
Conv7	128	32 × 32
Conv8	128	32 × 32
Max Pool	128	16 × 16
(Localization+Classification)		
Conv9	128	16 × 16
Conv10	128	16 × 16
Conv11	128	$5^2 \times 3 + (5 \times 5)$
Accuracy (mAP)	69.1	

## Implementation

### Implementation

- **FPGA board: NetFPGA-SUME**
  - FPGA: Virtex7 VC690T
  - LUT: 427,014 / 433,200
  - 18Kb BRAM: 1,235 / 2,940
  - DSP48E: 0 / 3,600
- **Realized the pre-trained NRNS-based YOLOv2**
  - 9 bit fixed precision (dynamic range: 30 bit)
- **Synthesis tool: Xilinx Vivado2017.2**
  - Timing constrain: 300MHz
  - 3.84 FPS@3.5W → 1.097 FPS/W



## Evaluation

### Comparison



	NVidia Pascal GTX1080Ti	NetFPGA-SUME
Speed [FPS]	20.64	3.84
Power [W]	60.0	3.5
Efficiency [FPS/W]	0.344	1.097

# Design Automation

S. Antão and L. Sousa, “The CRNS framework and its application to programmable and reconfigurable cryptography,” ACM Transactions on Architecture and Code Optimization, vol. 9, no. 4, pp. 33:1–33:25, 2013.

# Design Automation

## Implementation Details

- For a processor with word length of  $2l$  bits, choose moduli of the form:

$$m_i = 2^l - c_i$$

- The following congruence is valid:

$$z = z_0 + 2^l z_1 \equiv z_0 + c_i z_1 \pmod{m_i}$$

- Enabling fast reductions if  $c_i$  is small



# Design Automation

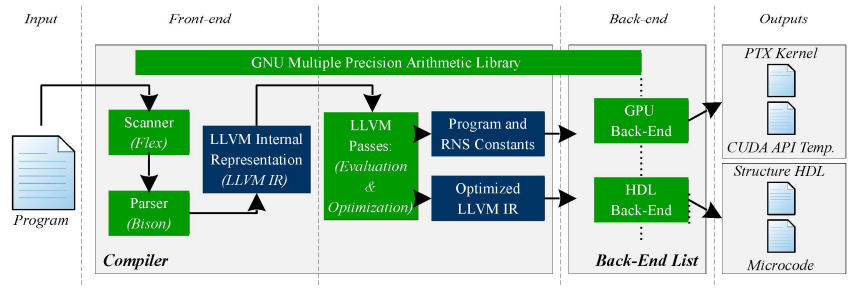
## Implementation Details

- To maximize the hardware reuse in Montgomery's Reduction algorithm:
  - Associate each Processing Element (PE) with a channel from the first base and with a channel from the second base
  - PEs can be SIMD channels on CPUs, threads on GPUs, or hardware elements on FPGAs

# Design Automation

## Compute with Residue Number System

- Obtains in seconds a fully functioning RNS based implementation
- RNS details are transparent to the user



## Compute with Residue Number System (DSL)

- Algorithm described using an extended version of C
- Available at <http://sfantao.net/index.php/prototypes>

```

01. (outputA) = (int1024 inputA)
02. {
03.     /*modulo:*/
04.     int N = 0xbf4325a19 ... c92e820f7;
05.     /*exponent:*/
06.     int D = 0x922ad9c67 ... c1c856434;
07.     /*mask:*/
08.     int M = 0x400000000 ... 000000000;
09.
10.     /*compute the exponentiation*/
11.     int I = inputA % N;
12.
13.     int R = I;
14.     while(M)
15.     {
16.         int M2 = M & D;
17.
18.         R = (R * R) % N;
19.
20.         if(M2)
21.             R = (R * inputA) % N;
22.
23.         M = M >> 1;
24.     }
25.
26.     outputA = R;
27. }

```

## CRNS

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← → ↻ 🏠 [sfantao.net/index.php/prototypes/9-prototypes/17-cms-computing-with-the-residue-number-...](http://sfantao.net/index.php/prototypes/9-prototypes/17-cms-computing-with-the-residue-number-...) ...

Cryptography & Block... (2) How does a block... Dona Emelinda Reser... Weiqiang Liu Synergy Grants | ERC... AE PACT 2017 ANI - Agência Nacion... Atlantic Area

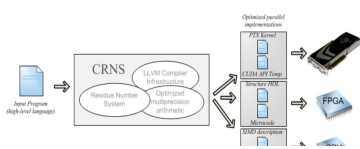
**SAMUEL ANTÃO** Web Page

You are here: [Home](#) > [Prototypes](#) > [Prototypes](#) > [CRNS: Computing with the Residue Number System Framework](#)

**CRNS: Computing with the Residue Number System Framework**

**Description:**

The CRNS framework consists of a comprehensive set of tools ranging from a programming language and respective compiler to backends targeting parallel computation platforms such as GPUs and reconfigurable hardware supporting applications based on Modular Arithmetic. The aim of CRNS is to automate the design for this class of applications, exploiting the increasing parallelism available in nowadays computing devices. The parallelism is obtained by employing optimized routines based on the Residue Number System in the resulting implementations. Given an input algorithm described with an high-level language, any designer can obtain in a few seconds a fully functional parallel implementation for the CRNS supported devices.



# On-going Projects

<https://futuretpm.eu/>



## FutureTPM (2018-2020)

- FutureTPM aims to design QR cryptographic algorithms
  - Symmetric Cryptography
  - Asymmetric Cryptography
  - Privacy-protecting primitives, such as Direct Anonymous Attestation
- Hardware, **Software**, and Virtual TPM FutureTPM
- Standardization within TCG, ISO/IEC and ETSI
- Run-Time Risk Assessment and Vulnerability Analysis

## Post qUaNTum Cryptography TooLbox (PUNCTuaL) PUNCTual(2018-2019): UPMC-ULisboa

- Cryptographic solutions mainly based on public key using the RSA and ECC primitives.
  - These primitives are insecure if a quantum computer is produced
- PUNCTuaL provides tools and methodologies for PQ cryptography on which secure and efficient protocols can be underpinned
- Design of the tools will be based on industrial use-cases
  - protocols used to secure the digital market, telecommunications and the Internet of Things (IoT).

## Conclusions

- Residue Nंबर System improves performance of multiple cryptosystems
- RNS helpful in updating inefficient RSA systems to ECC, and also in moving to quantum-resistant cryptography
- RNS is also helpful for Artificial Intelligence, namely CNNs
- Applicable to a wide-range of platforms, since channel bit-width can be tailored for the target architecture (also embedded systems)
  - (e.g. GPUs, CPUs with SIMD, FPGAs)

Thank You  
for your attention!

technology  
from seed

