

1st Workshop on AutotuniNg and aDaptivity AppRoaches for Energy efficient HPC Systems (ANDARE'2017)

Saturday, September 9, 2017		Room TBD
12:00 PM - 1:00 PM	Lunch	
1:00 PM - 1:05 PM	Opening Session, by Andrea Bartolini, University of Bologna, Italy	
1:05 PM - 2:00 PM	Keynote : "Proactive Control of Approximate Programs" by Keshav Pingali (Univertity of Texas at Austin, USA) Chair: Andrea Bartolini, University of Bologna, Italy	
Paper Session	Session A: Invited Presentations Chair: Andrea Bartolini, University of Bologna, Italy	Authors
2:00 PM - 2:30 PM	<i>Exploiting parallelism on GPUs and FPGAs with OmpSs</i>	Xavier Martorell, BSC, Spain
2:30 PM - 3:00 PM	<i>Adaptive Performance Sensitivity Model to Support GPU Power Management</i>	Francesco Paterna,Ujjwal Gupta, Raid Ayoub,Umit Ogras, Michael Kishinevsky, Intel, USA
3:00 PM - 3:30 PM	Coffee Break	
Invited Papers Session	Session B: Presentations of Regular Papers Chair: Francesco Paterna, Intel, USA	Authors
3:30 PM - 3:55 PM	<i>Auto-tuning Static Schedules for Task Data-flow Applications</i>	Andreas Diavastos and Pedro Trancoso, University of Cyprus, Cyprus
3:55 PM - 4:20 PM	<i>Autotuning of OpenCL Kernels with Global Optimizations</i>	Jiří Filipovič, Filip Petrovič and Siegfried Benkner, University of Vienna, Austria, and Masaryk University, Czech Republic
4:20 PM - 4:45 PM	<i>Benefits in Relaxing the Power Capping Constraint</i>	Daniele Cesarini, Andrea Bartolini and Luca Benini, University of Bologna, Italy, and ETHZ, Switzerland
4:45 PM - 4:50 PM	Closing Session, by Andrea Bartolini, University of Bologna, Italy	